

Solution Fundamentals Of Logic Design Charles Roth

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Chapter 1.1: Introduction to logic

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$B = 1$, the latch state is $Q = 1$, $Q=1$, $Q = 1$, (e) Derive a circuit for the $A B AB A B$ latch using four two-input NOR gates and two inverters. An $A B ? AB \ A B$ latch operates as follows: If $A = 0 ? A=0 \ A = 0$ and $B = 0, ? B=0, \ B = 0$, the latch state is $Q = 0; ? Q=0; \$

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$DB=AD'+BD+BCD_{\{B\}}=AD'+BD+BCDB =AD'+BD+BC$. K-map for $DCD_{\{C\}}DC$ flip flop: The Boolean expression will be given by: $DC=CD+AD'+BC'D_{\{D\}}D_{\{C\}}=CD+AD'+BC'D_{\{D\}}DC =CD+AD'+BC'D_{\{D\}}$. K-map for $DDD_{\{D\}}DD$ flip flop: The Boolean expression will be given by: $DD=DD_{\{D\}}=DDD =D$.

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Truth tables and state tables still are used to specify the behavior of logic circuits, and Boolean algebra is still a basic mathematical tool for logic design. Even when programmable logic devices are used instead of individual gates and flip-flops, reduction of logic equations is still desirable in order to fit the

equations into smaller PLDs.

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Fundamentals of Logic Design Book Description: Updated with modern coverage, a streamlined presentation, and excellent companion software, this seventh edition of Fundamentals of Logic Design achieves yet again an unmatched balance between theory and application. Authors Charles H. Roth, Jr. and Larry L. Kinney carefully present the theory that is necessary for understanding the fundamental concepts of logic design while not overwhelming students with the mathematics of switching theory.

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