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Architecture 80386 Operating
mode: Real mode, Protected
Mode and Virtual Mode

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~~Architectures ? — See How a
CPU Works~~ ISA 1.1

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The basic Purpose of Real
mode in 80386 is to set up

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the processor for Protected Mode Operation. Memory Addressing in Real Mode of 80386 In Real mode the max. memory size is limited to 1MB, so only address line A2-A19 are used. In Real addressing mode the linear

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addresses are the same
physical addresses as paging
is not allowed.

*32-bit Microprocessor-Intel
80386*

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Chapter 17 80386 Instruction

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Set. This chapter presents instructions for the 80386 in alphabetical order. For each instruction, the forms are given for each operand combination, including object code produced, operands required, execution

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time, and a description. For each instruction, there is an 80386 Instruction Set ...

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80386 Specific Instruction

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Set:• The base instruction set was enhanced in the 80286 microprocessor with a group of instructions known as the extended instruction set. • All these instructions are also available in the 80386 real mode. • The

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enhancement to the 80386dx's
real mode instruction set is
the 80386 specific
instruction set. •

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Instruction set of 8086 1.

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2. Instruction Set of 8086

An instruction is a binary pattern designed inside a microprocessor to perform a specific function. The entire group of instructions

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that a microprocessor
supports is called
Instruction Set. 8086 has
more than 20,000
instructions.21-Nov-2010
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IInnssttrruuccttiioonn

sseett ooff 88008866

MMiiccrroopprrroocceesssoorr

1 2. SSooffttwwaarree • The

sequence of commands used to

tell a microcomputer what to

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do is called a program, •
Each command in a program is
called an instruction • A
program written in machine
language is referred to as
machine code 2 ADD AX, BX
(Opcode) (Destination
operand) (Source ...

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Chapter 17 80386 Instruction Set. This chapter presents instructions for the 80386 in alphabetical order. For each instruction, the forms

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are given for each operand combination, including object code produced, operands required, execution time, and a description. For each instruction, there is an operational description and a summary of exceptions

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generated.

*80386 Programmer's Reference
Manual -- Chapter 17*

The Intel 80386, also known
as i386 or just 386, is a
32-bit microprocessor
introduced in 1985. The

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first versions had 275,000 transistors and were the CPU of many workstations and high-end personal computers of the time. As the original implementation of the 32-bit extension of the 80286 architecture, the 80386

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instruction set, programming model, and binary encodings are still the common denominator for all 32-bit x86 processors, which is termed the i386-architecture, x86, or IA-32, depending o

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original implementation of
the 32-bit extension of the
80286 architecture, the
80386 instruction set,

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combination, including object code produced, operands required, execution time, and a description. For each instruction, there is an operational description and a summary of exceptions generated.

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Manual -- Table of Contents
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Central Processing Unit

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Instruction Set The 80386
can execute all 16-bit
instructions in real and

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protected modes. This is provided in order to make the 80386 software compatible with the 8086. The 80386 uses either 8- or 32-bit displacements and any register as ...

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instruction set ; 1997 Intel
announced that it would
expand the Pentium and the
Pentium Pro architecture
with MMX. This new set of 57
instructions uses the

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floating-point stack to
accelerate multimedia and
communication applications.

5 REGISTERS AND DATA

ADDRESSING. The evolution of
the instruction set can be
seen in the registers of the
80386.

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*PPT - THE INTEL 80x86
Microprocessor PowerPoint*

...

ENHANCED INSTRUCTION SET OF
A 80386 PROCESSOR The
instruction set of 80386
contains all the

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instructions supported
by 80286. The 80286
instructions are designed to
operate with 8-bit or 16-bit
data, while the same
mnemonics for 80386
instruction set may be
executed over 32-bit

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operands, besides 8-bit and
16-bit operands.

*ENHANCED INSTRUCTION SET OF
A 80386 PROCESSOR*

Let us take a look at the
programming of 8085
Microprocessor. Instruction

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sets are instruction codes to perform some task. It is classified into five categories. Write a program to arrange first 10 numbers from memory address 3000H in an ascending order. MVI B, 09 : "Initialize counter"

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START : "LXI H ...

Microprocessor - 8085

Instruction Sets -

Tutorialspoint

Following is the list of
instructions under this
group ? REP ? Used to repeat

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the given instruction till CX ? 0. REPE/REPZ ? Used to repeat the given instruction until CX = 0 or zero flag ZF = 1. REPNE/REPNE ? Used to repeat the given instruction until CX = 0 or zero flag ZF = 1.

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